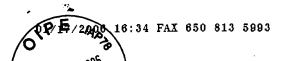
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MORRISON & F 755 PAGE MILL F PALO ALTO, CA	FOERSTER LLP RD 94304-1018			Ce I hereby certify that to States Postal Service addressed to the Ma transmitted to the US	rtificate of Mailing or Transhis Fee(s) Transmittal is bein with sufficient postage for fir il Stop ISSUE FEE address PTO (571) 273-2885, on the	mission g deposited with the Unite st class mail in an envelop above, or being facsimil late indicated below.
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APPLICATION NO. 10/713,751	11/13/2003		Bruce W. M		1881220001800	1624
TITLE OF INVENTION: S	YSTEM AND METHOD FO	R ADAPTIVE PAI	RTTTIONING	OF CIRCUIT COMPONEN	ITS DURING SIMULATION	
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Comments:

Attorney Docket No.: 188122001800

Group Art Unit: 2825 Examiner: T. Do Serial No. 10/713,751

Filing Date: November 13, 2003

Inventor(s): Bruce W. McGAUGHY et al.

Title: SYSTEM AND METHOD FOR ADAPTIVE PARTITIONING OF CIRCUIT COMPONENTS

DURING SIMULATION

Papers Attached:

1. Part B - Fee(s) Transmittal (1 page + copy)